

*D<sup>1</sup> cont*

a dielectric interposed between said first and second conductive capacitor plates, wherein said dielectric is an oxide of a metal layer overlying the first conductive capacitor plate; and

a processor configured to access the monolithic memory device.

*D<sup>2</sup>*

79. (Once Amended) The capacitor of claim 19, wherein the [first conductive plate] metal layer is formed from at least one metal selected from the group consisting of titanium, copper, gold, tungsten, and nickel.

*D<sup>3</sup> G<sup>1</sup>*

81. (Once Amended) The capacitor of claim 19, wherein the second conductive capacitor plate is formed from a material selected from the group consisting of polysilicon and metal.

*D<sup>3</sup> Sub<sup>3</sup>*

82. (Once Amended) The memory system of claim 20, wherein the [first conductive plate] metal layer is formed from at least one metal selected from the group consisting of titanium, copper, gold, tungsten, and nickel.

*D<sup>4</sup> G<sup>1</sup>*

84. (Once Amended) The memory system of claim 20, wherein the second conductive capacitor plate is formed from a material selected from the group consisting of polysilicon and metal.

*D<sup>4</sup> Sub<sup>4</sup>*

85. (Once Amended) The capacitor of claim 53, wherein the [first capacitor electrode] metal layer is formed from at least one metal selected from the group consisting of titanium, copper, gold, tungsten, and nickel.

*D<sup>5</sup> G<sup>1</sup>*

104. (Twice Amended) A capacitor, comprising:

a first conductive plate serving as a first electrode of the capacitor [formed from at least one metal selected from the group consisting of titanium, copper, gold, tungsten, and nickel, alloyed with at least one additional metal selected from the group consisting of strontium, barium, and lead];

a second conductive plate serving as a second electrode of the capacitor, the second

conductive plate formed from a material selected from the group consisting of polysilicon and metal; and

a dielectric interposed between the first and second conductive plates, wherein the dielectric is an oxide of a metal layer overlying the first conductive plate, the metal layer formed from at least one metal selected from the group consisting of titanium, copper, gold, tungsten, and nickel, alloyed with at least one additional metal selected from the group consisting of strontium, barium, and lead.

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D cont  
105. (Twice Amended) A memory system, comprising:

a monolithic memory device comprising a capacitor, wherein the capacitor comprises

a first conductive capacitor plate [formed from at least one metal selected from the group consisting of titanium, copper, gold, tungsten, and nickel, alloyed with at least one additional metal selected from the group consisting of strontium, barium, and lead],

a second conductive capacitor plate formed from a material selected from the group consisting of polysilicon and metal, and

a dielectric interposed between the first and second conductive plates, wherein the dielectric is an oxide of a metal layer overlying the first conductive plate, the metal layer formed from at least one metal selected from the group consisting of titanium, copper, gold, tungsten, and nickel, alloyed with at least one additional metal selected from the group consisting of strontium, barium, and lead; and

a processor configured to access the monolithic memory device.

106. (Twice Amended) A capacitor comprising:

a first capacitor electrode [formed from] comprising polysilicon [at least one metal selected from the group consisting of titanium, copper, gold, tungsten, and nickel, alloyed with at least one additional metal selected from the group consisting of strontium, barium, and lead];

a dielectric layer formed by oxidizing a metal layer overlying the first capacitor electrode, the metal layer formed from at least one metal selected from the group consisting of titanium, copper, gold, tungsten, and nickel, alloyed with at least one additional metal selected from the group consisting of strontium, barium, and lead; and

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a second capacitor electrode formed from a material selected from the group consisting of polysilicon and metal.

D8 GI

113. (Once Amended) The capacitor of claim 104, wherein the [metal layer comprises titanium] first conductive plate comprises polysilicon having a thickness of 200 to 400 Angstroms.

D8 GI

115. (Once Amended) The memory system of claim 105, wherein the [metal layer] first conductive capacitor plate comprises [titanium] polysilicon.

GI/D8

117. (Once Amended) The capacitor of claim 106, wherein the [metal layer] first capacitor electrode has a thickness from 200 to 400 Angstroms [comprises titanium].

D8 GI

119. (New) A capacitor structure formed on a substrate, comprising:  
a first conductive capacitor plate formed atop the substrate;  
a first metal layer formed atop the first conductive capacitor plate;  
a first metal oxide layer formed from the metal layer such that the remaining first metal layer forms part of the first conductive capacitor plate; and  
a second conductive layer formed atop the first metal oxide layer.

120. (New) The capacitor structure of claim 119, further including:  
a second metal layer formed atop the second conductive layer;  
a second metal oxide layer formed from the second metal layer such that the remaining second metal layer forms part of the second conductive layer;  
a third conductive layer formed atop the second metal oxide layer, wherein the first and second metal oxide layers and the second conductive layer form the dielectric of the capacitor and the third conductive layer serves as a second conductive capacitor plate.

121. (New) The capacitor structure of claim 119, wherein:  
the first conductive capacitor plate comprises polysilicon and the first metal layer

comprises a metal selected from the group of metals consisting of titanium, tungsten, copper, gold, and nickel.

122. (New) The capacitor of claim 119, wherein the first metal layer is substantially completely oxidized to form the metal oxide layer.

123. (New) The capacitor of claim 119, wherein the first metal oxide layer has a thickness of between 10 and 1000 Angstroms.

124. (New) The capacitor of claim 119, wherein the first metal layer is alloyed with another material.